

SUMAN HALDAR

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TEACHING EXPERIENCE

Total teaching experience 13 years 9 months, as per follows

- 1) As a lecturer in dept of electronics and communication engineering in Saroj Mohan Institute of Technology (Under Techno India group) from 19.07.2005 to 22.07.2006.
- 2) As a lecturer in electronics and communication engineering dept in Institute of Technology and Marine Engineering (ITME) from 25.07.2006 to 31.07. 2010
- 3) As an assistant professor (Sr Grade) in Dept of electronics and communication engineering in Neotia Institute of Technology Management and Science (NITMAS) from 1st august 2010 to till now.

Award and achievement

- 1) Best Research Scholar Paper award by IEEE WIE section for paper "Soumita Haldar Chakraborty, Suman Haldar "Design and Analysis of Ternary Logic Gates and Combinational Circuits in 180nm CMOS" in NCTS-2K17 sponsored by IEEE WIE Kolkata section and IEI.
- 2) NPTEL online certification (Roll No-NPTEL18CS08S1511965) on Internet of Things with a consolidated score of 79%.

Professional course / Workshop/ Conferences

1. Completed training in the year of 2004 in the field of cellular communication and network from BSNL India.
2. Participated in QIP short term course sponsored by AICTE on "III-V compound semiconductor device technology at IIT kharagpur in 2009.
3. Completed short term course on "Telecommunication networks with state of the art hands on experiments " in 2010 under the guidance of Prof Debashis Dutta from IIT kharagpur.
4. Completed 2 days workshop on " Quality Enhancement of Engineering Education Through NPTEL" in 2011 from IIT kharagpur
5. Attend "CALCON 2011" organised by IEEE Calcutta section and its CAS chapter at jadavpur university.
6. Attend young scientist Colloquium 2014 at IEST shibpur.
7. Attend national conference on " research trend in computer application" in November,2015 as an author at Siliguri Institute of Technology organised by Computer society of India and ISTE as
8. Completed CISCO certified networking courses on routing and switching in April'2016.
9. Attend NCTS-2K17 conference ,technically sponsored by IEEE WIE Kolkata section and IEI as an author.
10. Participated in International Conference on Renewable and Alternate Energy at Assam science and Technology University at Guwahati Assam in decmber,2018 as an author.

RESEARCH PUBLICATION:- Prof Suman Haldar already have published journal and conference paper in IEEE, computer society of India, American Institute of physics like reputed research publication society.

1. **Suman Haldar**, Soumita Haldar Chakraborty, Amitava Sinha, Pizush Biswas "LOW POWER CONFIGURABLE MODULATOR USING TERNARY LOGIC, "International journal of Engineering Science and Technology, ISSN:0975-5462, vol.7 No3 ,Mar 2015.
2. Chandan Deb, Arindam Sen ,**Suman Haldar** "Customized APN based medical application using 3G and 2.5G network, "National conference on research trends in computer science and application organised by CSI & ISTE, ISBN 978-93-85477-34-8, Nov 2015.
3. Suman Haldar, Soumita Haldar Chakraborty, Pradipto Maity "Implementation and comparative study of a High-Speed Multimode Digital Modulator for Power Constrained Digital Communication" International journal of Engineering Science and Technology, ISSN : 0975-5462 Vol. 8 No.07 Jul 2016
4. Soumita Haldar Chakraborty, Suman Haldar "Design and Analysis of Ternary Logic Gates and Combinational Circuits in 180nm CMOS" International Journal of Scientific & Engineering Research, Volume 8, Issue 3, March-2017, ISSN 2229-5518, in NCTS-2K17. technically sponsored by IEEE WIE Kolkata section and IEI.
5. Suman Haldar, Soumita Haldar Chakraborty "Implementation and Study of High Speed Multimode Digital Modulator Using Ternary Logic" .The international conference on " Emerging Trends in Electronic Devices and Computational Techniques; EDCT2018 " at GNIT campus on 8th March'2018 technically sponsored by IEEE Kolkata section DOI:IEEE- 10.1109/EDCT.2018.8405056.
6. Suman Haldar, Ratan Mandal and Arindam Mondal, "Impact of Clean Transportation Systems in Rural Economy: A Study' International Conference on Renewable and Alternate Energy (ICRAE-2018).04 – 06 December, 2018. Organized by ASSAM SCIENCE AND TECHNOLOGY UNIVERSITY .Conference proceeding published in American Institute of Physics(AIP).

SUBJECT TAUGHT

1. Basic electronics engineering 2. Analog electronics 3. Digital electronics 4. Telecommunication switching and network 5. Analog communication 6. Microprocessor and microcontroller 7. Wireless communication and network 8. Data communication and networking 9. Internetworking technology 10. Electronics measurement and instrumentation.

HARDWARE SKILLS & INNOVATIVE LABORATORY EXPERIMENT

1. Short circuit protection based variable regulated power supply using BD140 power transistor and PM 129A DPM module.
2. Design and spectrum analysis of QPSK modulator using CD4520, CD4512 and mux and frequency divider IC.
3. Design of digital voltmeter for measurement of 1-30 volt DC using P89V51RD2 micro controller.
4. Design of low cost Op-amp based digital multi meter using PM129A DPM module.

5. Detection of ASCII character of keyboard and half duplex communication based data transmission using ATC51 microcontroller and PC-XT machine interfacing with the help of serial asynchronous communication control word configuration.
6. Design of configurable digital modulator using CD4007 Dual Complementary Pair plus Inverter IC.
7. P89V51RD2 microcontroller based digital multimeter for measurement of resistance, current voltage and temperature with more than 90% accuracy.
8. Design of microcontroller less line follower robot using LM 358, L293D ,LDR and combinational circuit.
9. Design and performance analysis of high speed look ahead carry generator and adder using tanner EDA tool.
10. Design and development of a heart rate measuring smart device using HRM 2511E module.
11. Non destructive quality evaluation technology for fruits and vegetables using MQ series sensor.
12. As a project guide successfully completed 14 undergraduate and 2 post graduate project based on communication engineering , computer network and embedded systems .

TEACHING METHODOLOGIES FOLLOWED

1. Course file 2. Power point presentation 3. Quiz 4. Fortnightly assignment 5. Periodic test

SOFTWARE SKILLS

Windows 98/ 2000/XP, windows 7, windows 8 and windows 10 2. Microsoft office word, excel and power point version 7 onwards 3. Latex 4. Tanner EDA Tool 5. KEIL IDE including Flash magic tool for 8051 series 6. CISCO packet tracer 7. Circuit maker and SPICE 8. HFSS 9. GNS3 10. Orcad 11.MAT LAB.

OTHER RESPONSIBILITIES AND PROFESSIONAL ACTIVITIES

1. As a reviewer of book “ Microprocessor and Microcontroller ” by Soumitra kumar Mandal published by Mc-Graw Hill education in 2012
2. 1st year subject coordinator of basic electrical and electronics engineering ES201 and ES291 (theory and lab respectively) from 2013 onwards.
3. Organizer and subject teacher of winter/summer school running after semester recess under Centre for development in embedded system and VLSI design at NITMAS from 2012 onwards
4. Faculty coordinator and founder of club “Techniche”, The technical club of NITMAS since 2010
5. Faculty representative of Annual technical festival “Sankalp” at NITMAS since 2010
6. Mentoring 3rd year B.tech students of ECE since 2012.
7. Conducting WBUT exam as a departmental supervisor of Examination Cell.
8. Observer of Joint entrance exam conducted by WBJEEB.
9. WBUT and WBSCTE examiner since 2006

HOBBIES

Gardening, Collecting tropical colour fishes, Listening music and collecting audio CD's

Signature

Suman Khatz